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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Jeffrey R. Wilcox, et al.	§	Group Art Unit:	2187
Serial No.:	10/038,960	§	Examiner:	Christian Chase
Filed:	January 2, 2002	§	Atty. Dkt. No.:	ITL.0668US (P12986)
For:	Method and Apparatus For Reducing Power Consumption In A Memory Bus Interface By Selectively Disabling And Enabling Sense Amplifiers	§		

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REPLY TO FINAL OFFICE ACTION DATED SEPTEMBER 27, 2004

Dear Sir:

Please consider the comments in the following REMARKS section.

Date of Deposit:	November 29, 2004
I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 010-222-0205) on the date indicated above.	
Janice Myrick	